

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,824,905 B2
APPLICATION NO. : 10/043404
DATED : November 30, 2004
INVENTOR(S) : Masaharu Shioya et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 29, line 22, change "diodes D 41 and D41"

to --diodes D 41 and D 42--.

Column 34, line 55, change "1/m2" to --1/m²--.

Column 37, line 36, change "FIG. 66A"

to --FIG. 31A--.

Column 39, line 62, change "62" to --72--.

Column 40, line 30, change "FIGS. 28D to 28G"

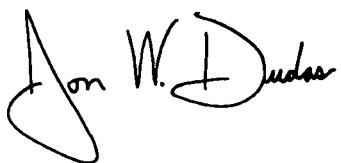
to --FIGS. 28D to 28F--.

Column 41, line 43, change "FIG. 48" to --FIG. 34--.

Column 42, line 11 to 12, delete ", as shown in FIGS. 49 and 60".

Signed and Sealed this

Twelfth Day of February, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office